

CURRENT STATUS OF THE CLAIMS

In the Claims

The following is a marked-up version of the claims with the language that is underlined (“ ”) being added and the language that contains strikethrough (“~~—~~”) being deleted:

1. (Twice Amended) A chip-level electronic package, comprising:
at least one monolithic waveguide having:
a waveguide core disposed in a fixed position on a lower cladding,
an air-gap cladding around a portion of the waveguide core, and
an overcoat layer engaging a portion of the air-gap cladding and engaging the lower cladding, wherein the air-gap cladding is completely bound on all sides by the overcoat layer, the lower cladding, and the waveguide core.
2. (Canceled)
3. (Previously Amended) The chip-level electronic package of claim 2 1, further comprising:
a lead; and
at least one air-gap layer disposed substantially under a portion of the lead and wherein the at least one waveguide is adjacent the air-gap layer.
- 4-5. (Canceled)
6. (Previously Amended) The chip-level electronic package of claim 1, further comprising:
a coupling element adjacent to the waveguide core and engaging the air-gap cladding.
7. (Original) The chip-level electronic package of claim 1, wherein the waveguide core includes at least one coupling element.

8. (Original) The chip-level electronic package of claim 7, wherein the at least one coupling element is a volume grating coupling element.
9. (Previously Amended) The chip-level electronic package of claim 7, wherein the air-gap cladding is disposed around a portion of one of the at least one coupling element.
- 10-14. (Canceled)
15. (Original) The chip-level electronic package of claim 1, wherein the waveguide core is adjacent to a lower waveguide cladding.
- 16-28 (Canceled)
29. (Twice Amended) A method of operating a chip-level electronic package comprising:
coupling an optical signal to a one monolithic waveguide in the wafer-level electronic package; and
communicating the optical signal through the waveguide, the waveguide having a waveguide core disposed in a fixed position on a lower cladding, an air-gap cladding around a portion of the waveguide core, and an overcoat layer engaging a portion of the air-gap cladding and engaging the lower cladding, wherein the air-gap cladding is completely bound on all sides by the overcoat layer, the lower cladding, and the waveguide core.
30. (Canceled)
31. (Previously Added) The chip-level electronic package of claim 1, wherein the overcoat layer is selected from silicon dioxide, silicon nitride, polyimides, polynorbornenes, epoxides, polyarylenes ethers, and parylenes.

32. (Previously Added) The chip-level electronic package of claim 1, wherein the overcoat layer is selected from polyimides, polynorbornenes, epoxides, polyarylenes ethers, and parylenes.
33. (Previously Added) The chip-level electronic package of claim 1, wherein the overcoat layer is selected from polyimides and polynorbornenes.
- 34-41. (Withdrawn)
42. (Newly Added) A chip-level electronic package, comprising:
at least one monolithic waveguide having:
a waveguide core disposed in a fixed position on a lower cladding,
an air-gap cladding around a portion of the waveguide core, and
an overcoat layer engaging a portion of the air-gap cladding and engaging the lower cladding, wherein the air-gap cladding is bound by the overcoat layer, the lower cladding, and the waveguide core.
43. (Newly Added) A method of operating a chip-level electronic package comprising:
coupling an optical signal to a one monolithic waveguide in the wafer-level electronic package; and
communicating the optical signal through the waveguide, the waveguide having a waveguide core disposed in a fixed position on a lower cladding, an air-gap cladding around a portion of the waveguide core, and an overcoat layer engaging a portion of the air-gap cladding and engaging the lower cladding, wherein the air-gap cladding is bound by the overcoat layer, the lower cladding, and the waveguide core.